



**Mrs. Merry Saxena (nee Sinha)**

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**PROFESSIONAL PREPARATION**

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<b>Ph.D. (Computer Science &amp; Engg.)</b>	<b>Pursuing</b>	Chitkara University Institute of Engineering and Technology, Chandigarh-Patiala National Highway, Punjab 140 401
<b>M.E. (Software Engineering)</b>	<b>2002</b>	Birla Institute of Technology (Deemed University), Mesra, Ranchi, India
<b>M.E. (Electrical &amp; Electronic Engg.)</b>	<b>2000</b>	Birla Institute of Technology (Deemed University), Mesra, Ranchi, India
<b>B.E. (Electrical &amp; Electronics Engg.)</b>	<b>1997</b>	Manipal Institute of Technology (Deemed University), Manipal, Karnataka

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**EXPERIENCE (+ 14 years)**

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<b>Duration</b>	<b>Designation</b>	<b>Place</b>
November 2015- till date	Associate Professor	Department of Computer Science & Engineering, Chitkara University Institute of Engineering and Technology, Chandigarh- Patiala National Highway, Punjab 140401
July 2009– October 2015	Assistant Professor	Department of Computer Science & Engineering, Chitkara University Institute of Engineering and Technology, Chandigarh- Patiala National Highway, Punjab 140401
September 2006-June 2009	Software Developer and Senior Faculty	National Institute of Information Technology (NIIT), Patiala Centre, Patiala
February 2006-August 2006	Sun Microsystems Project Fellow	Centre for Excellence in Grid Computing, Computer Science and Engineering Department (CSED), Thapar Institute of Engineering and Technology, Patiala
July 2003-December 2003	Lecturer (Adhoc)	Department of Electrical and Instrumentation Engineering, Thapar Institute of Engineering and Technology, Patiala

**Other Short term Assignments**

09 months	Faculty	INFORMATICS (affiliated to Cambridge University, London), Patna Branch
06 months (01 Semester)	Faculty (part-time)	Magadh Mahila College, Patna University, Patna (C++)
06 months (01 Semester)	Faculty (part-time)	Bihar College of Engineering (now National Institute of Technology), Patna (BCA Classes)
06 months (01 Semester)	Faculty (part-time)	Birla Institute of Technology, Mesra, Ranchi (Computer courses & Machine lab to ME Program)

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**SKILLS AND RESEARCH INTERESTS**

Proficiency in C, C++, C#, SQL, Unix, Oracle8i & Developer 2000, Power Builder, Visual Basic, Oracle 9.0. Logic Design, Digital Circuits, Core JAVA and software developmen , Data Base Management Systems, Artificial Neural Networking, Deep learning and Artificial Intelligence (AI)

**Projects Completed**

- Designing GENETIC ALGORITHM for optimization problem: A comparison with traditional optimization algorithm (Steepest Descent Method) [ 2002]
- Artificial Neural Network based voltage estimation of transmission networks [2000]
- Microprocessor based displacement measurement using linear variable differential transducer [1997]

**Publication**

Anchal Gupta, **Merry Saxena** and Rupali Gill (2018): Performance Analysis of RDBMS and Hadoop Components with

*their File Formats for the development of Recommender Systems. 3rd International Conference for Convergence in Technology (I2CT); The Gateway Hotel, XION Complex, Wakad Road, Pune, India. Apr 06-08, 2018. 978-1-5386-4273-3/18/\$31.00 ©2018 IEEE*

## **CONTACT**

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(MERRY SAXENA)